

www.xinxunwei.com 400-800-9990

**Revision : 3.01**

PAGE	TITLE
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```
APU_VDDIO_SUS=DDR15V
APU_VTT_SUS=DDRVTT
APU_VDDP_RUN=APU_VDDR_RUN=APU_VDDP
```

**+1.1V\_RUN=FCH\_VDD\_11\_RUN=VCC\_SB**

```
+3.3V_RUN=VCC3
+3.3V_ALW=3VDUAL
```

**Model Name:GA-F2A88X-D3H**

### Component value change history

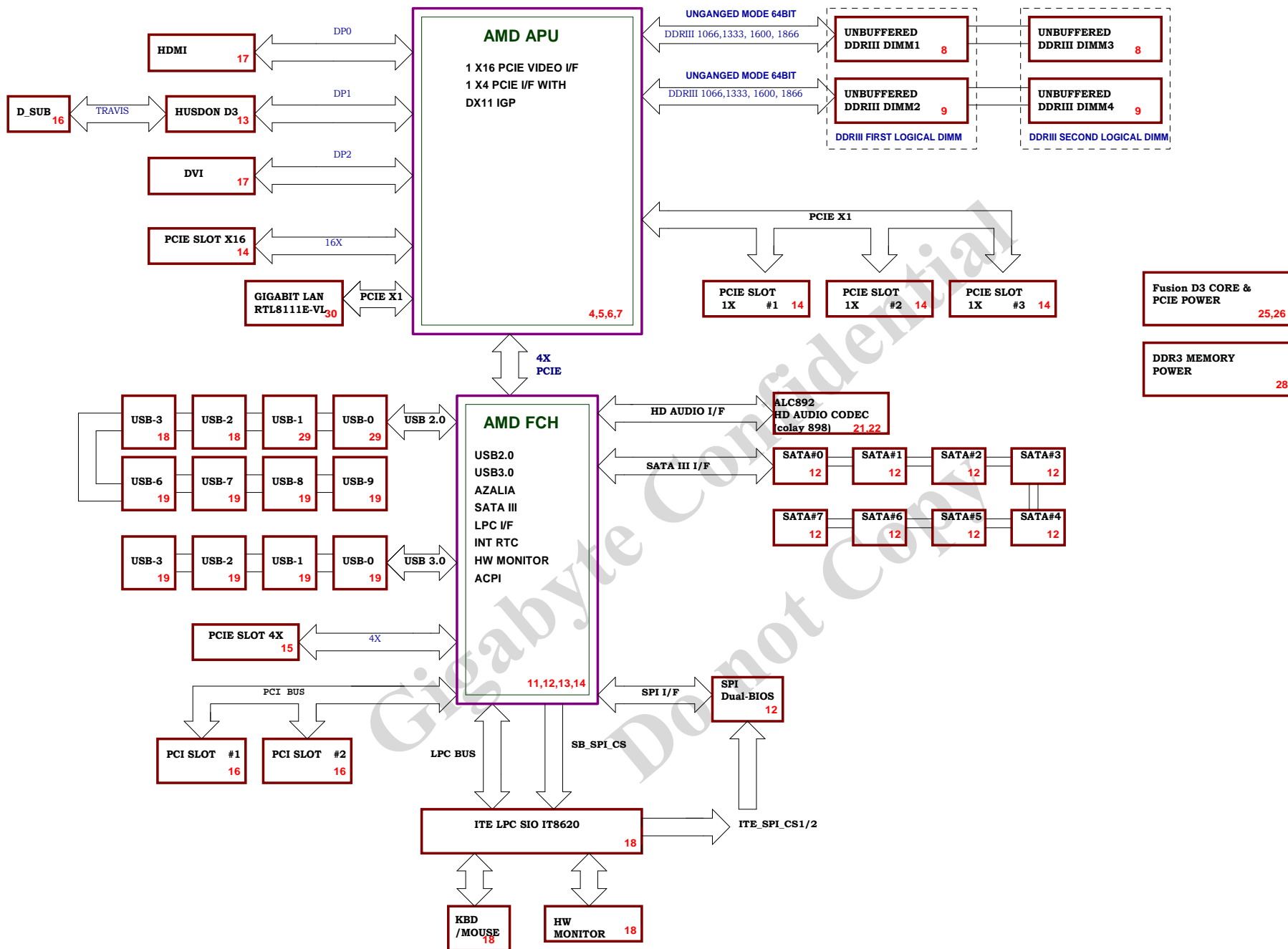
**Version: 3.01**

**P-Code: U98126-0**

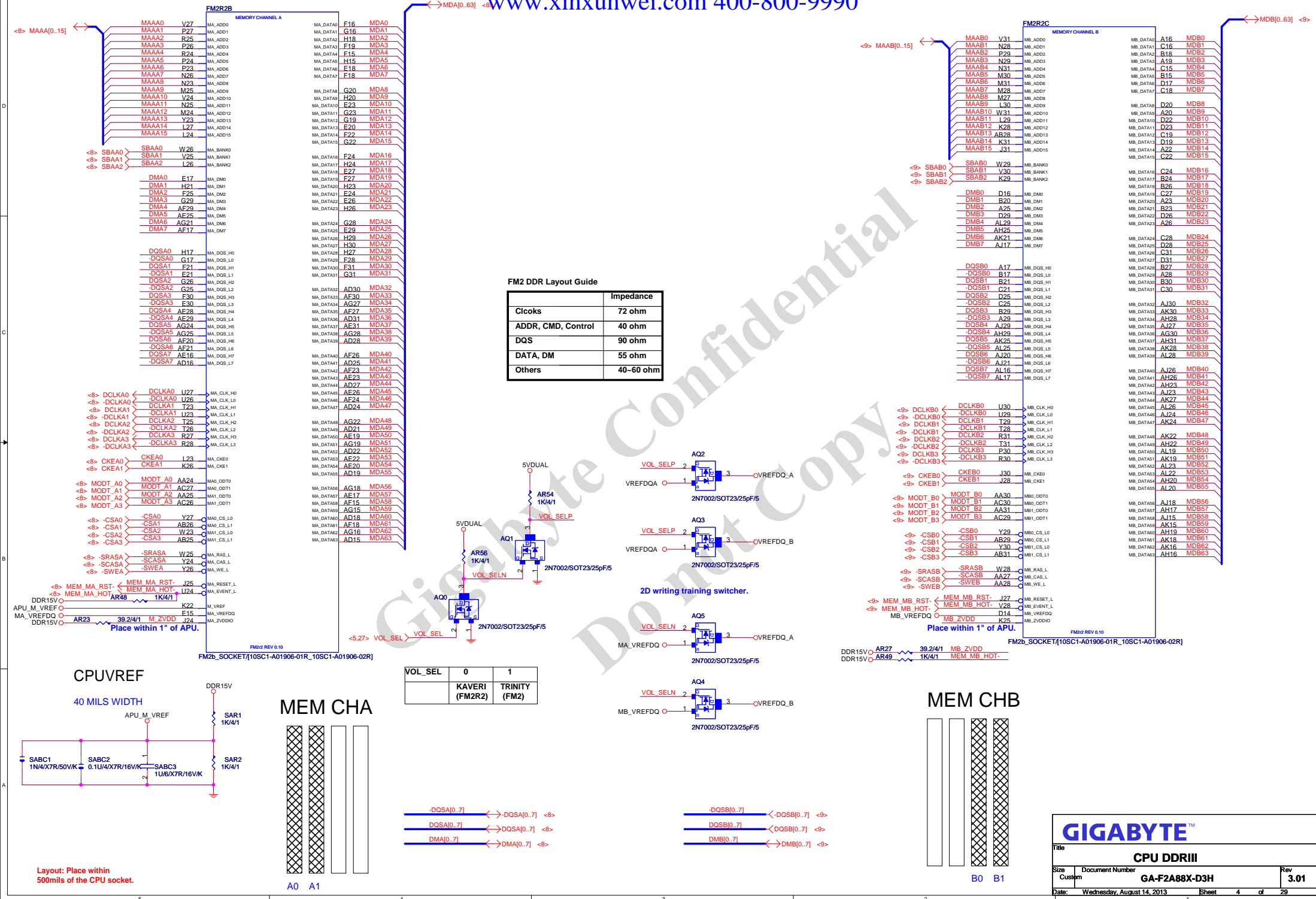
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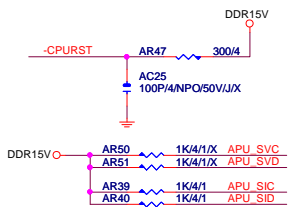
**Circuit or PCB layout change for next version**

[illegible]

**GIGABYTE™**

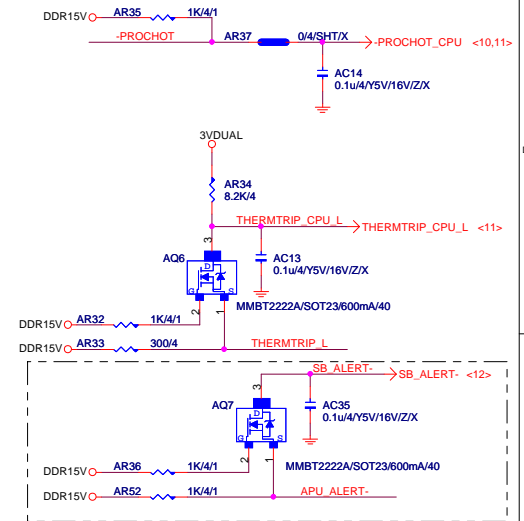
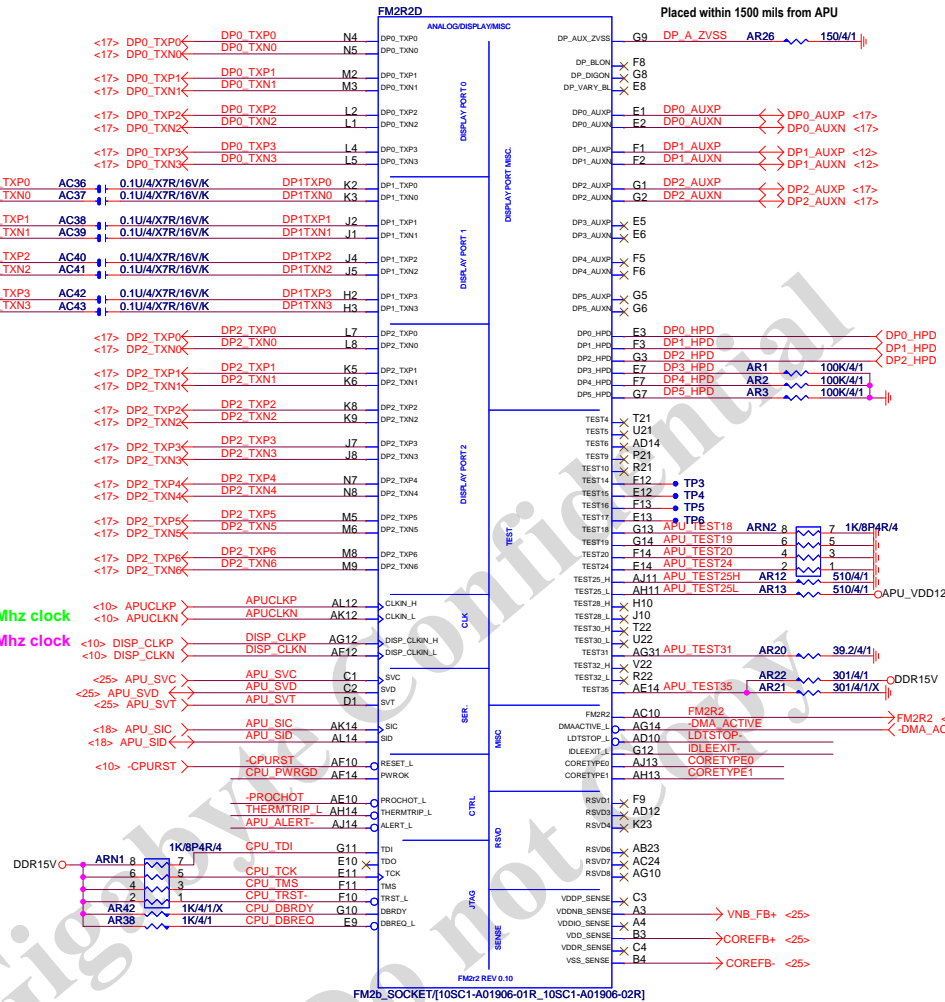
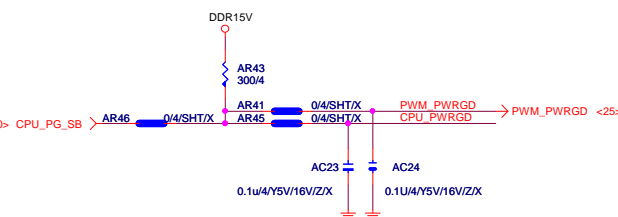
BLOCK DIAGRAM			
Title	BLOCK DIAGRAM		
Size	Document Number	Rev	
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SVI 2.0 max frequency=20MHz.  
Resolution=6.5mV

SVT=Serial VID Telemetry for  
APU get PWM information used.



**TEST35: high=>HDMI enable,  
low=>HDMI disable.**

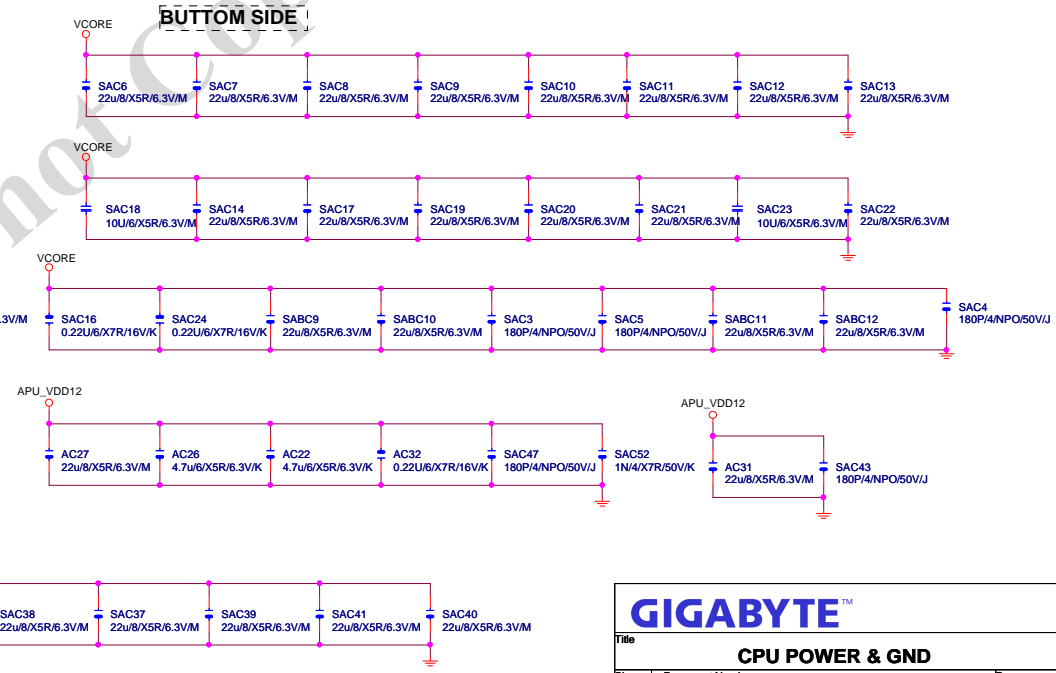
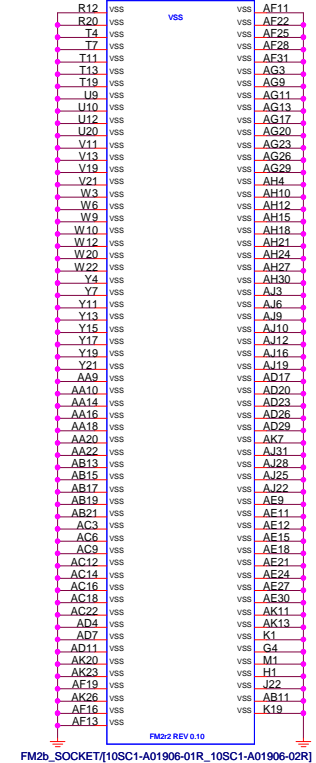
	APU FM2
Group A	VDDA25:1.8~2.7V/0.5A
Group A	DDR15V:0.8~2.3V/30A
Group B	VCORE: 0.8~2.0V/120A
Group B	VCORE_NB:1.2V/50A
Group B	VDDP:1.2V/5A
Group B	VDDR:1.2V/5A

**Group A must early than Group B**

APU	CORETYPE0	CORETYPE1
TRINITY	1	1
KAVERI	0	1
TBD	1	0
TBD	0	0

VOL_SEL	0	1
	KAVERI (FM2R2)	TRINITY (FM2)





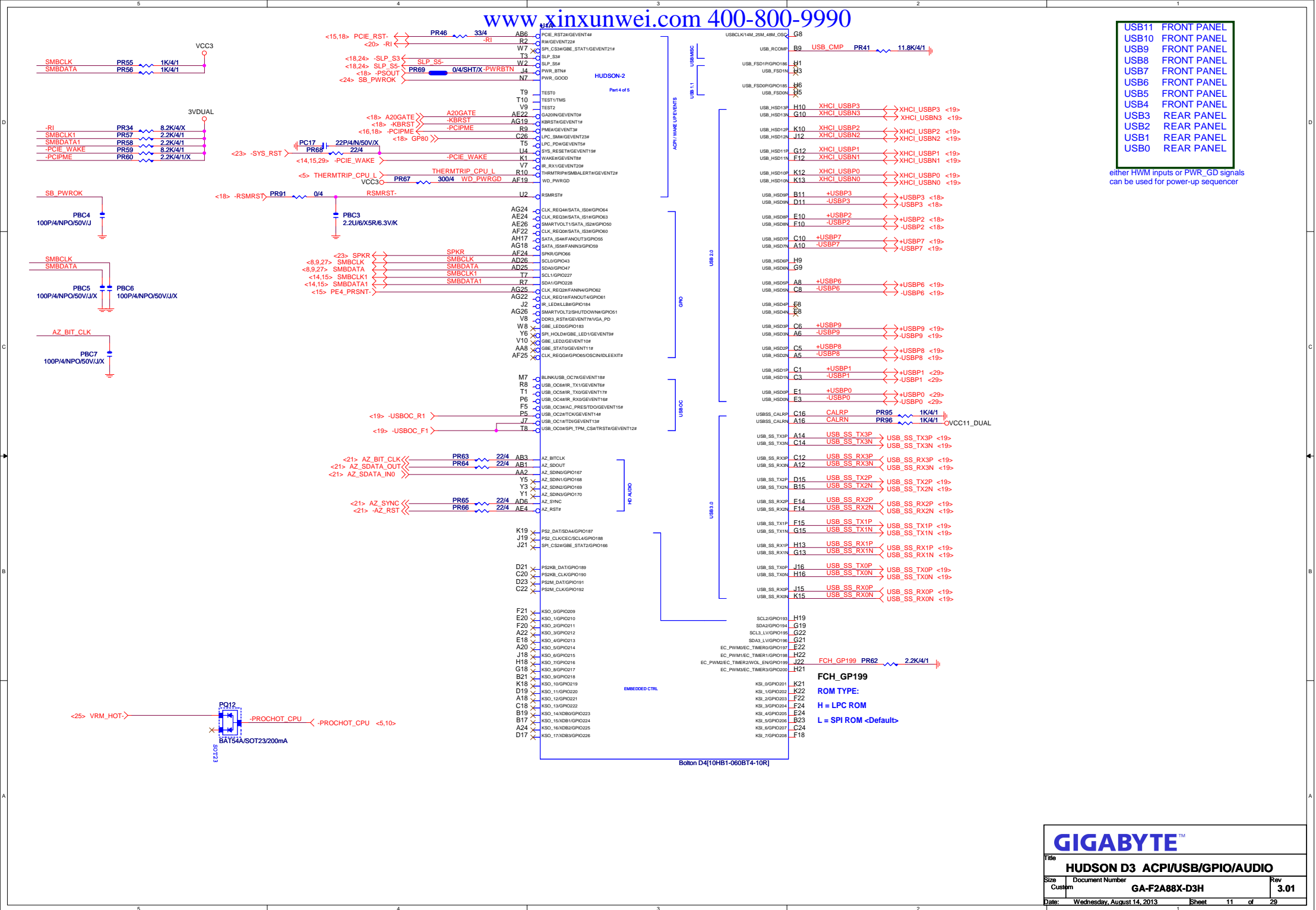




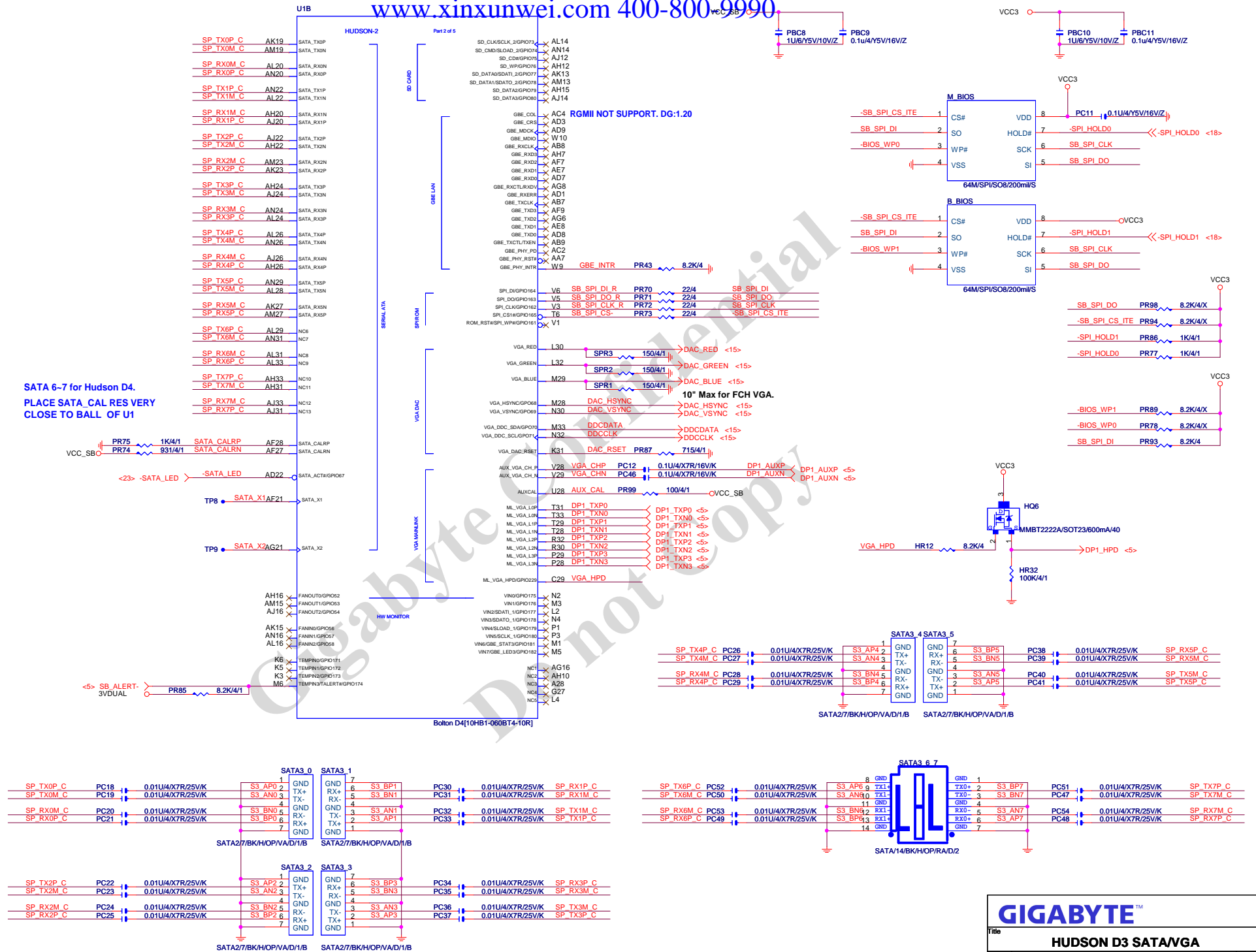






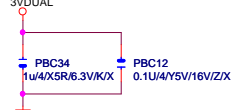
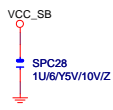
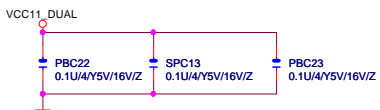
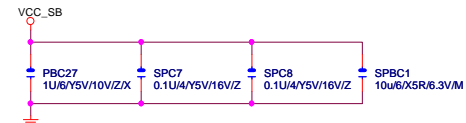
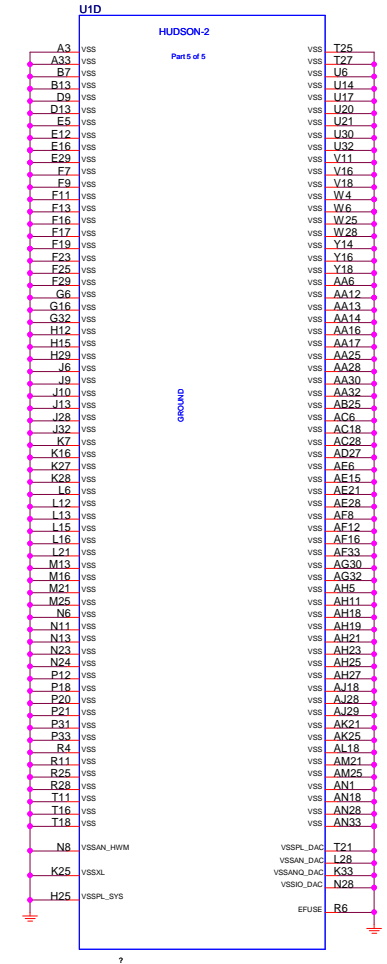
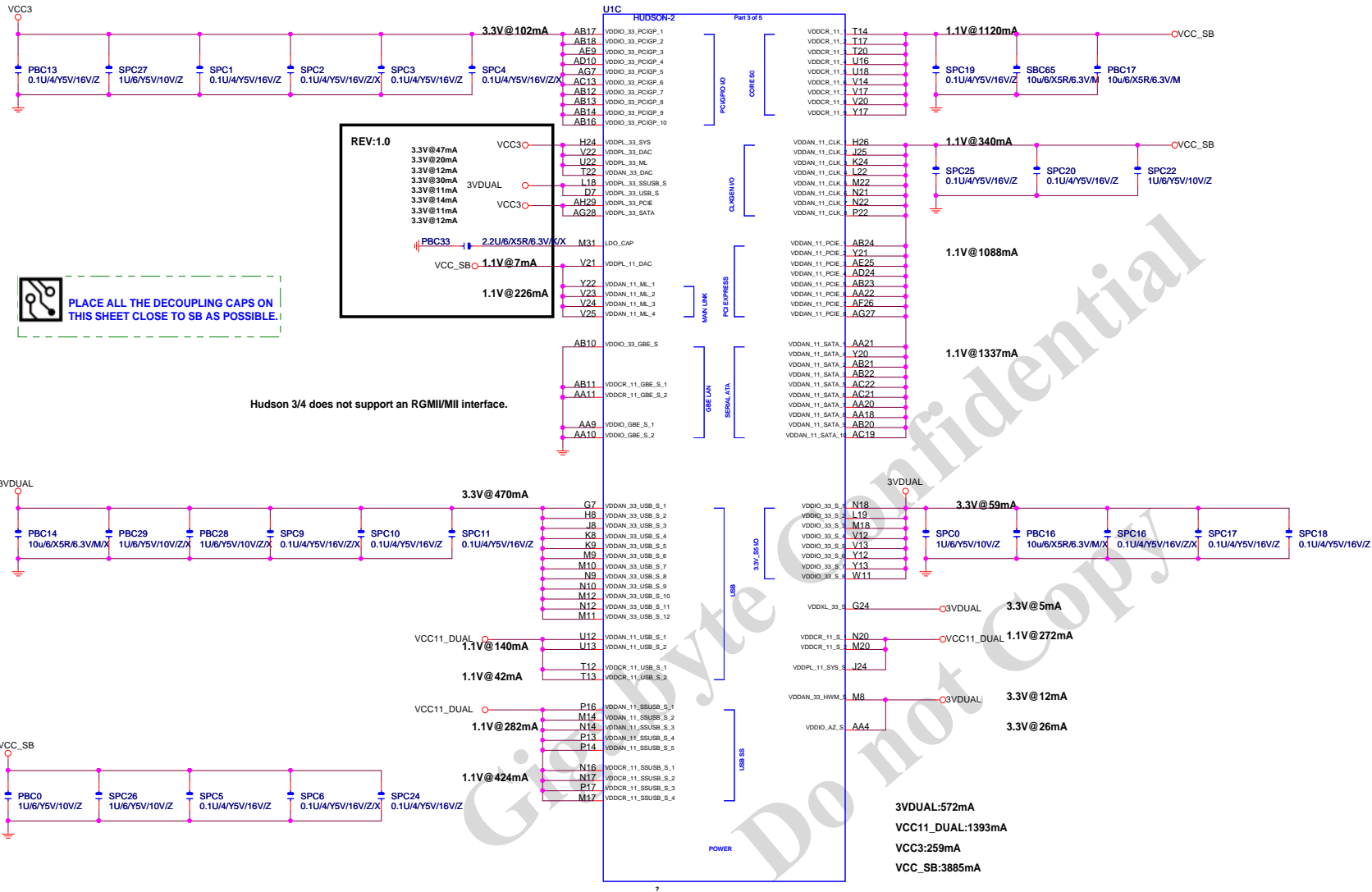


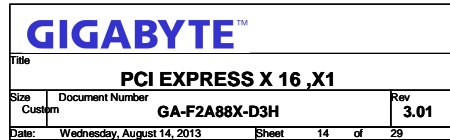
SATA 6-7 for Hudson D4.  
PLACE SATA\_CAL RES VERY  
CLOSE TO BALL OF U1



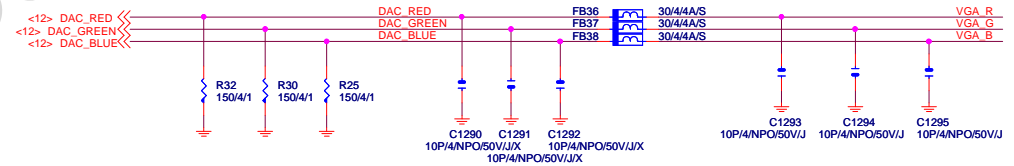
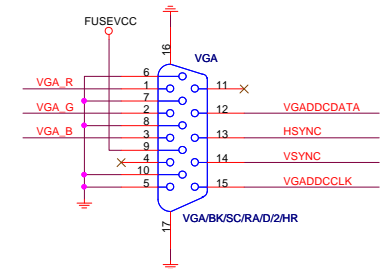
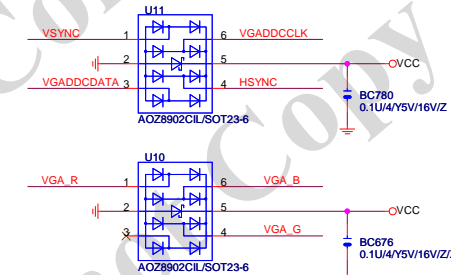
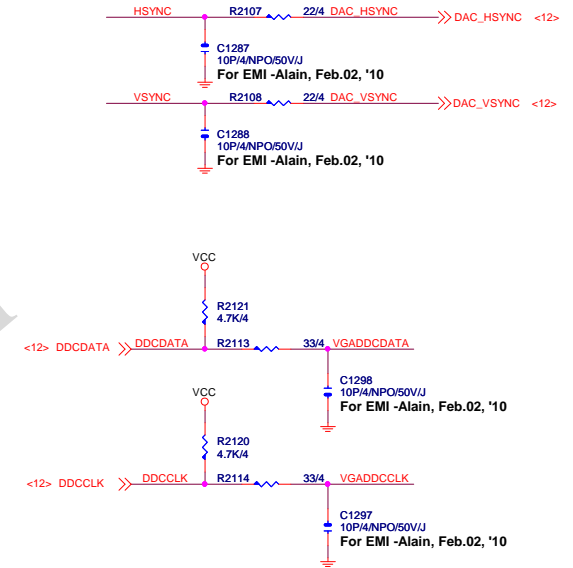
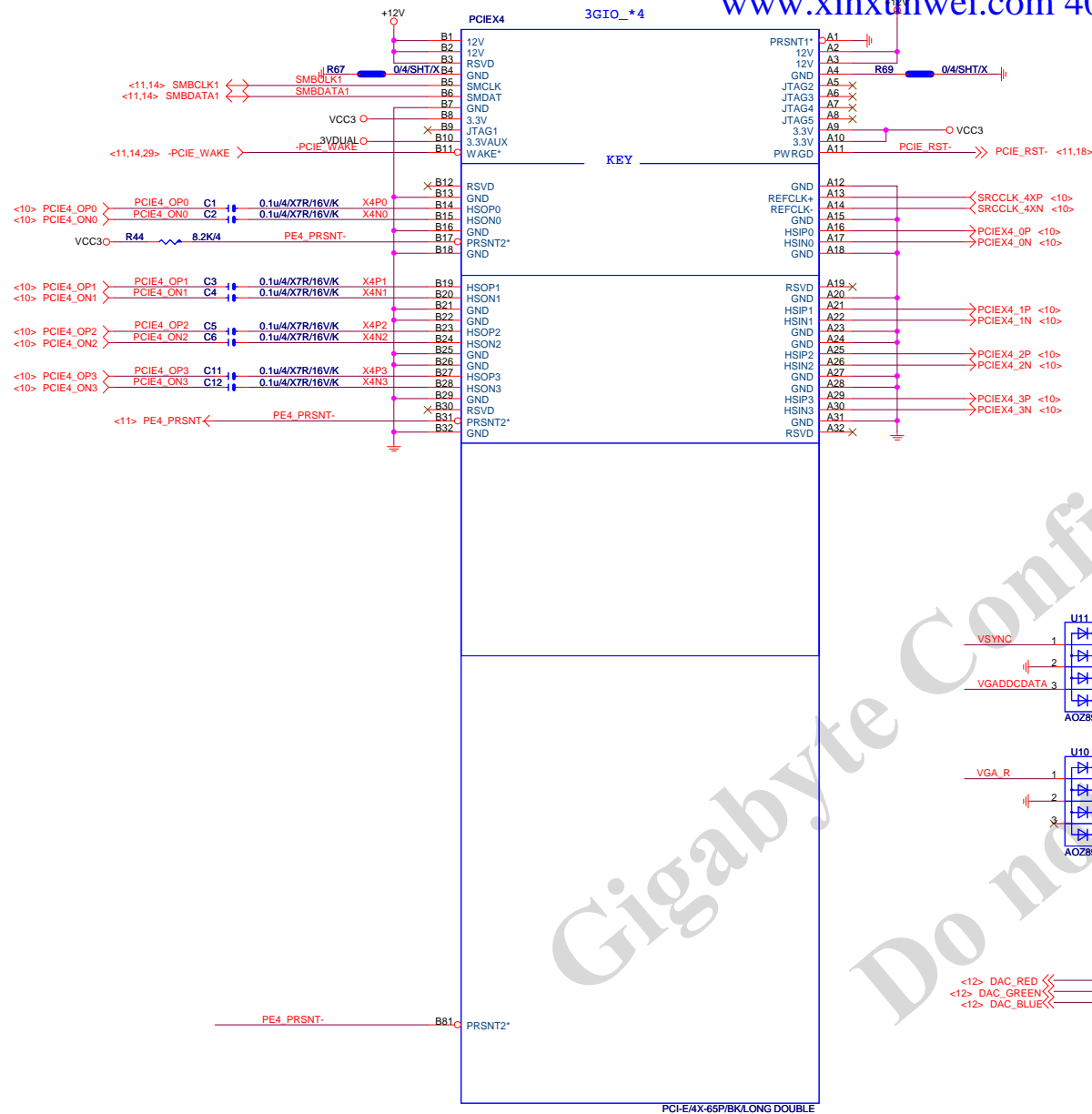
**GIGABYTE**

Title		
HUDSON D3 SATA/VGA		
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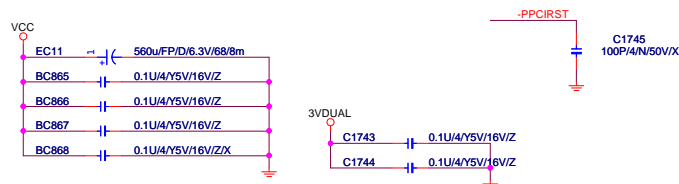
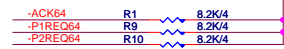
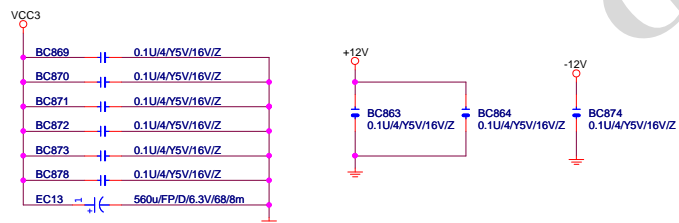
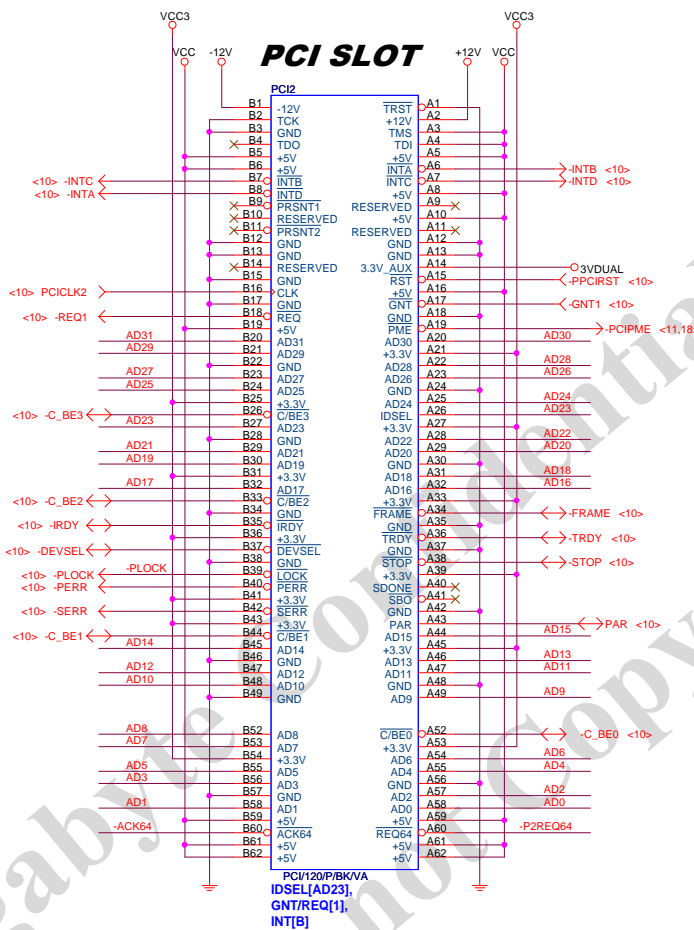
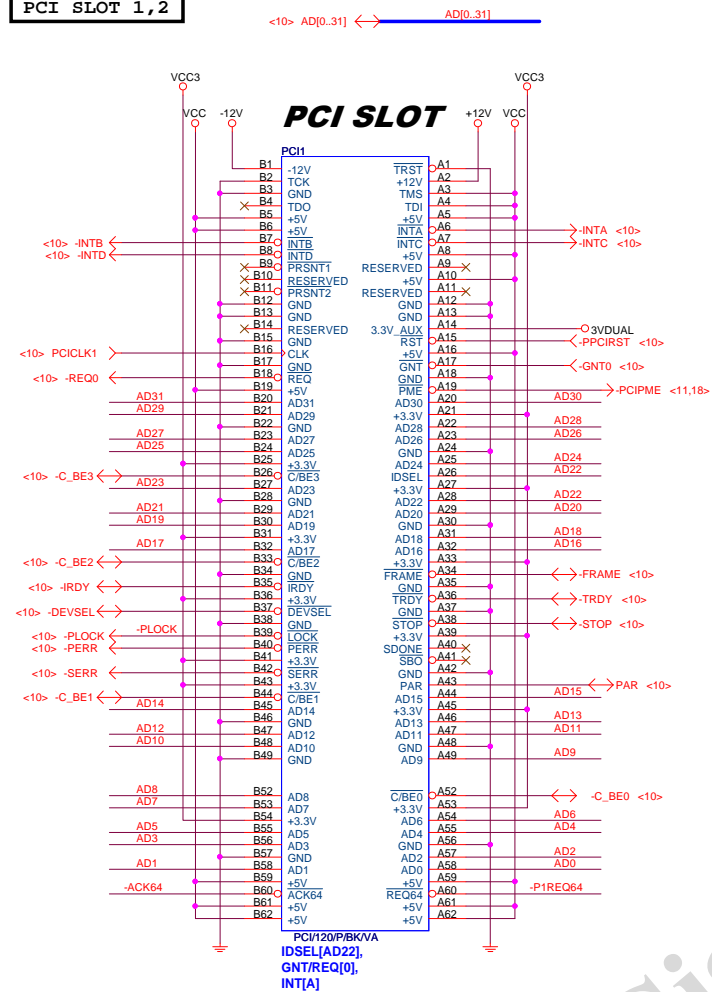



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Title		
PCIEX4/VGA		
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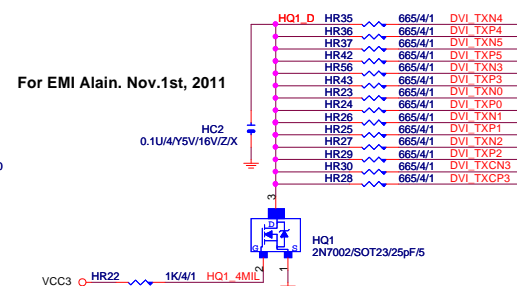
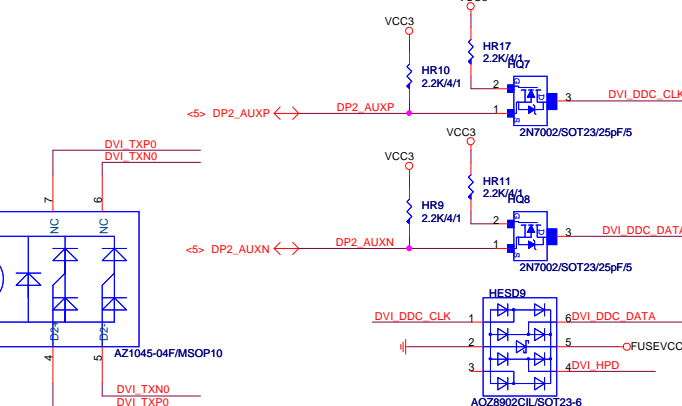
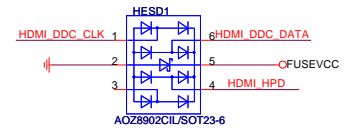
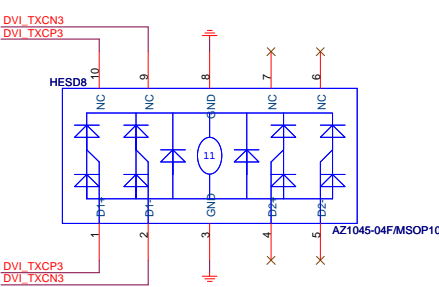
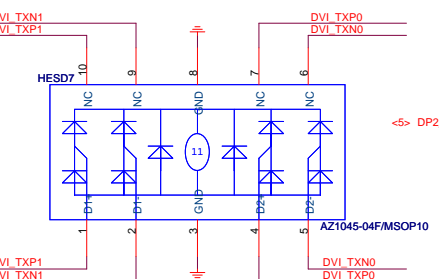
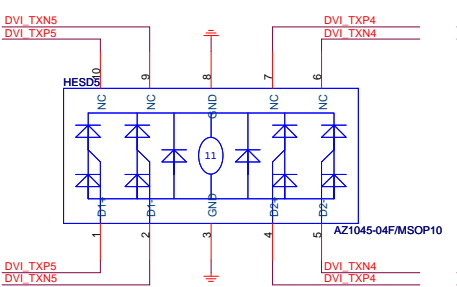
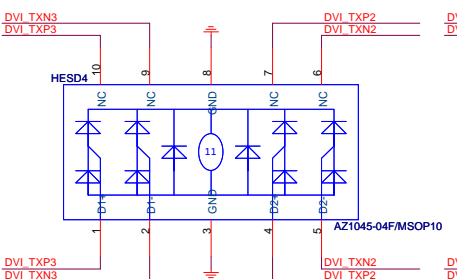
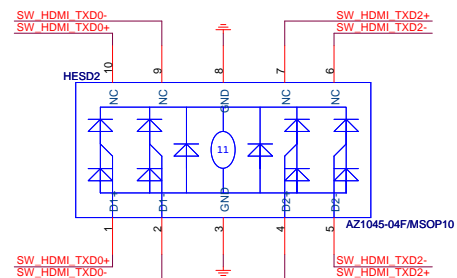
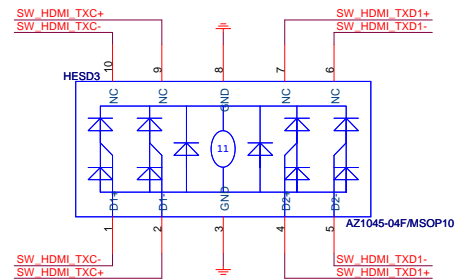
## PCI SLOT 1,2



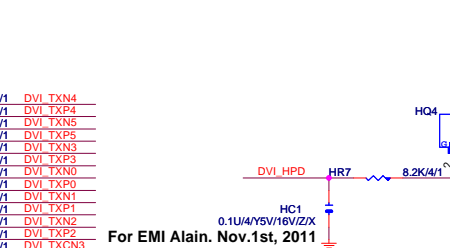
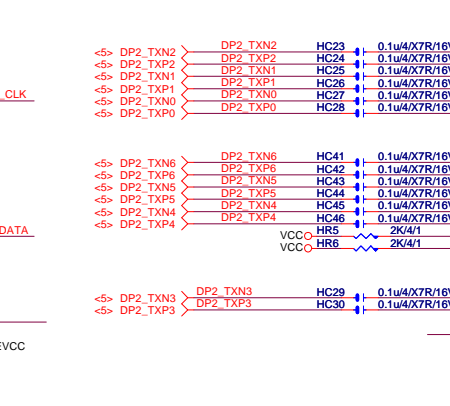
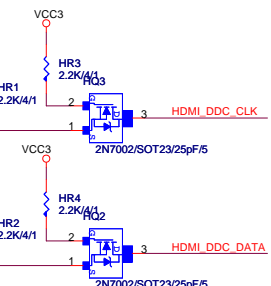
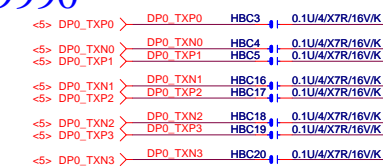
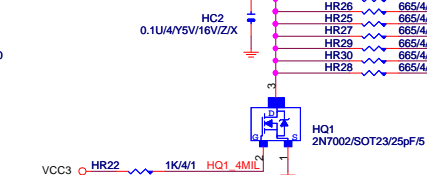
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Title  
**PCI SLOT**Size  
Custom  
Document Number  
**GA-F2A88X-D3H**Rev  
**3.01**

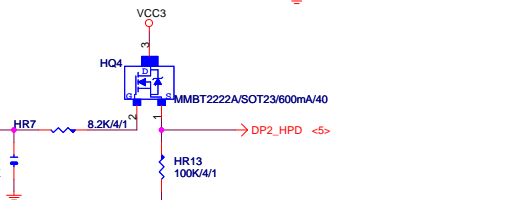
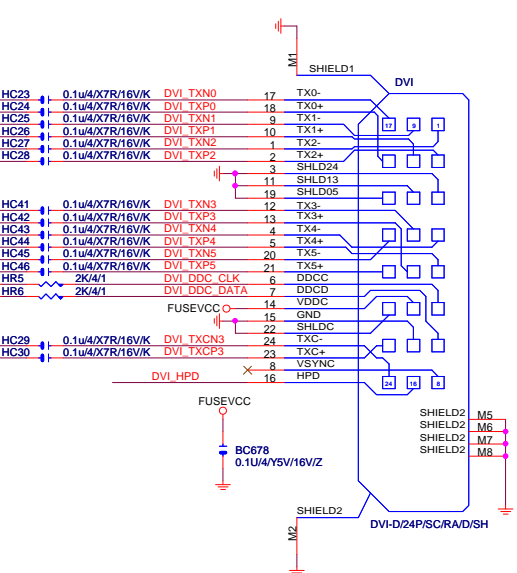
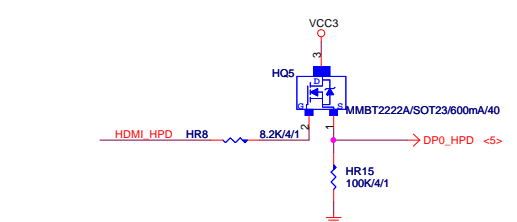
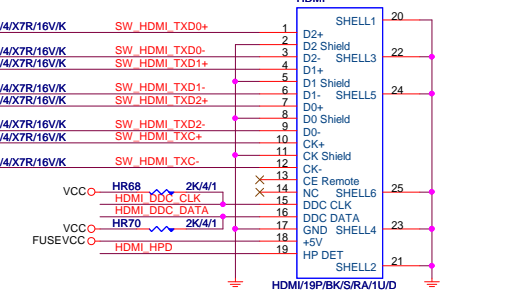
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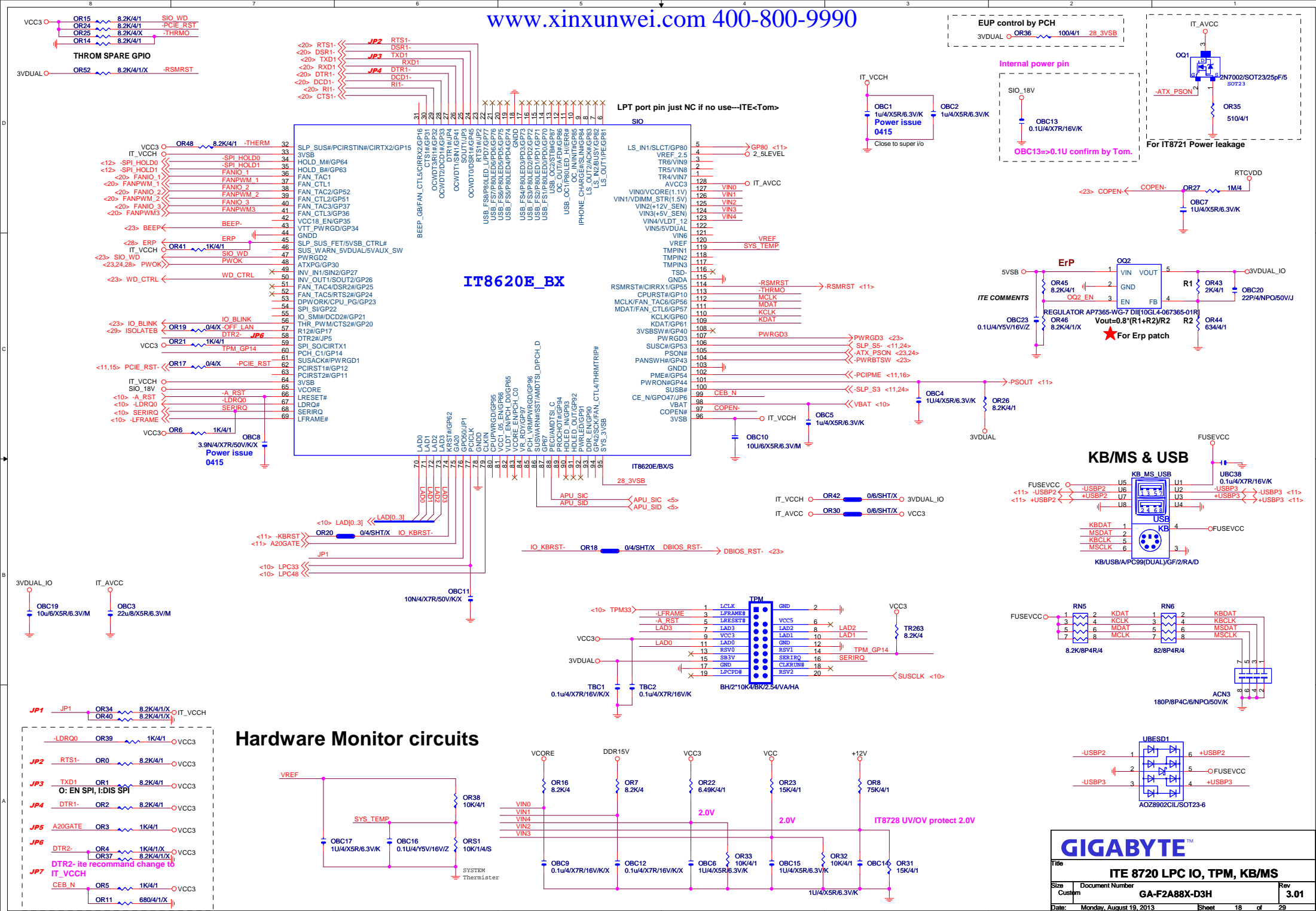


For EMI Alain. Nov.1st, 2011

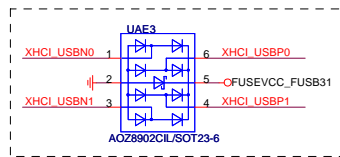
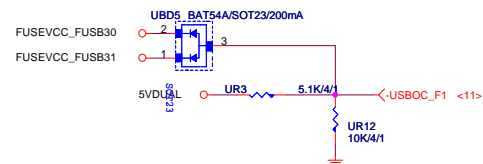
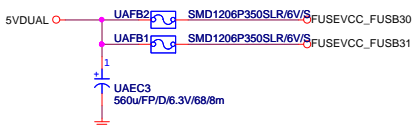
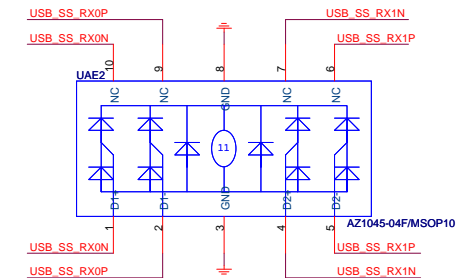
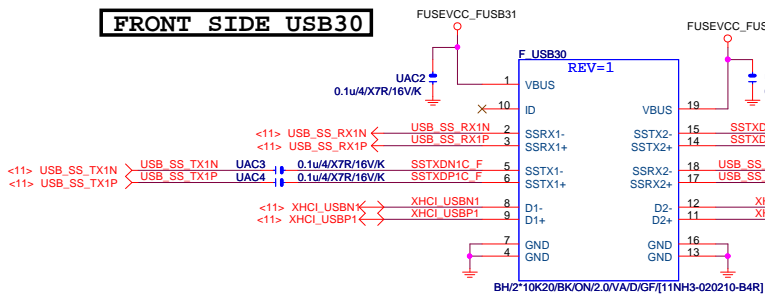


For EMI Alain. Nov.1st, 2011

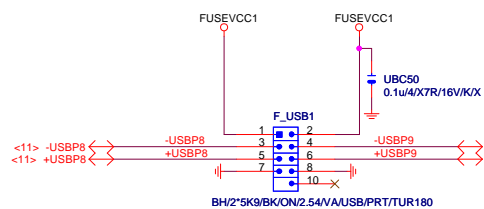




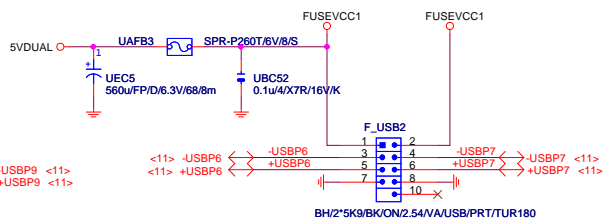
## FRONT SIDE USB30



## FRONT SIDE USB1

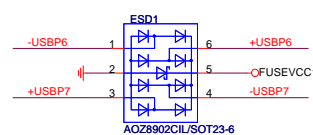
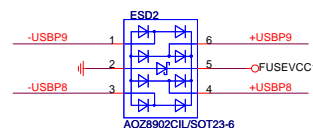
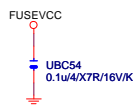
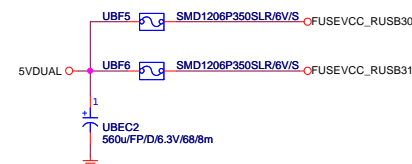
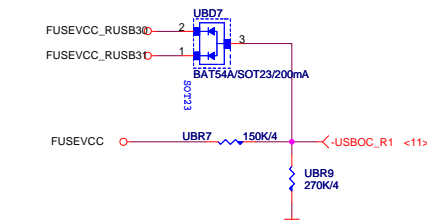
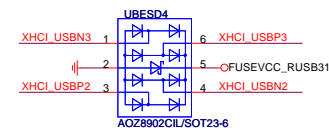
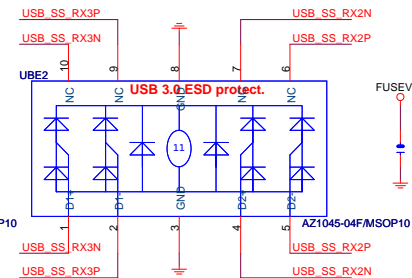
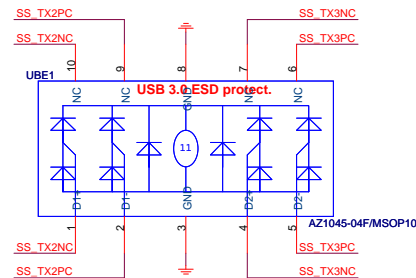
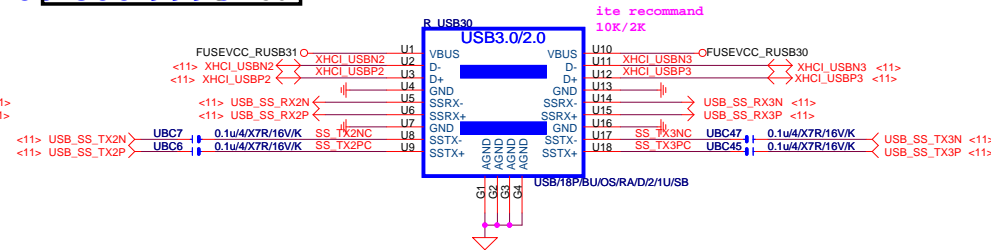


## FRONT SIDE USB2



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## REAR SIDE USB30



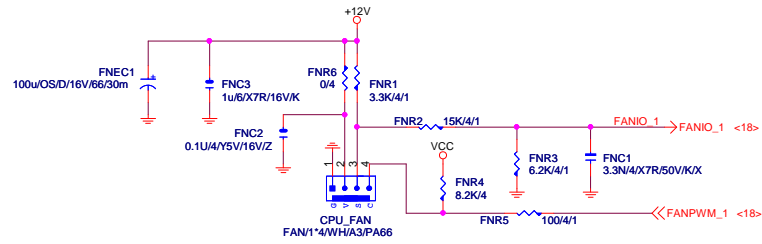
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F\_USB, ON/OFF CHARGE

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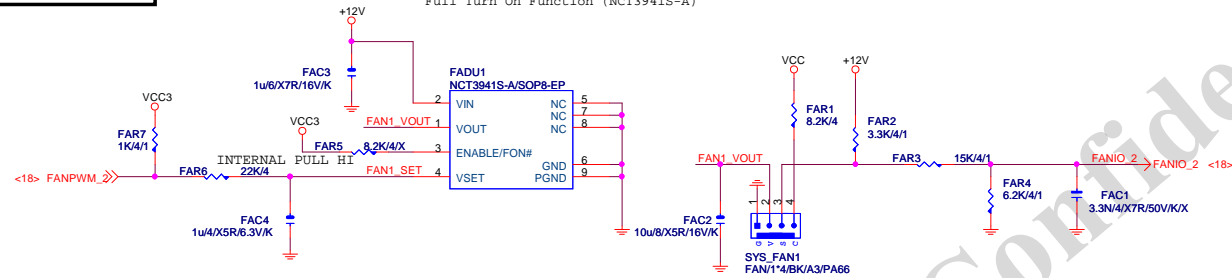
## CPU SMART FAN



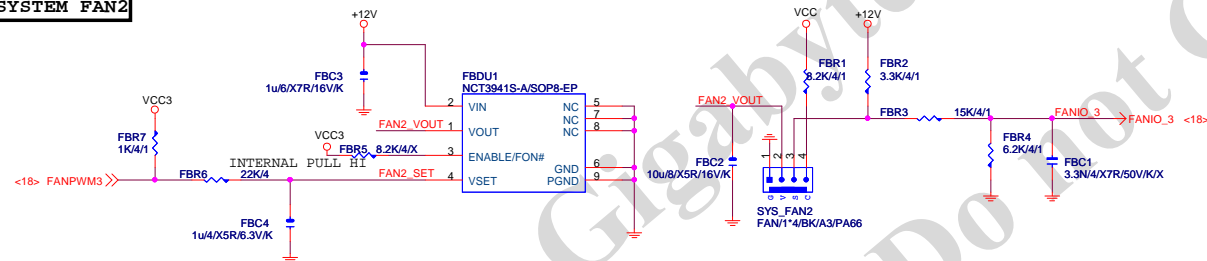
## SYSTEM FAN1

## Linear SYS\_FAN

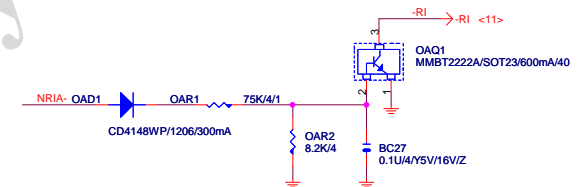
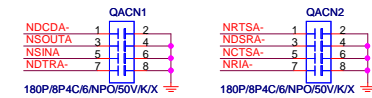
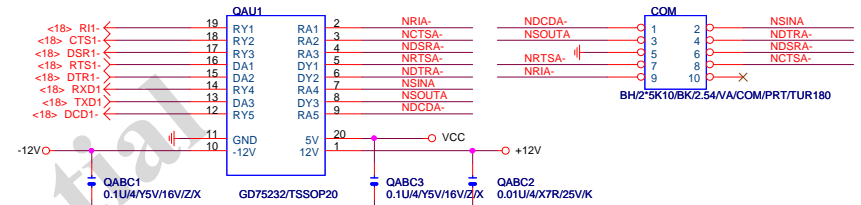
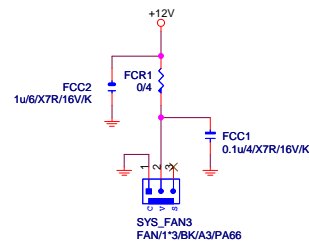
Enable Function (NCT3941S)  
Full Turn On Function (NCT3941S-A)



## SYSTEM FAN2



## SYSTEM FAN3



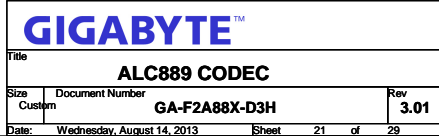
GIGABYTE™

Title  
FAN,HWMO, COMSize  
Custom Document Number  
GA-F2A88X-D3HRev  
3.01

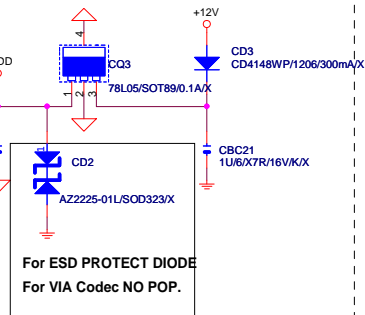
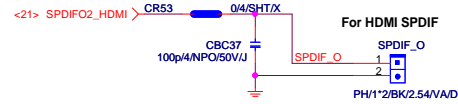
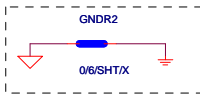
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CR36: 20K/4/1 @Realtek cdec & VT1708S-CE  
CR36: 5.1K/4/1 @VIA codec VT1708S-CD/VT2021  
CBC38 100P @VIA codec VT1708S

CBC60 100p/50V/μX  
CR36 20K/4/1

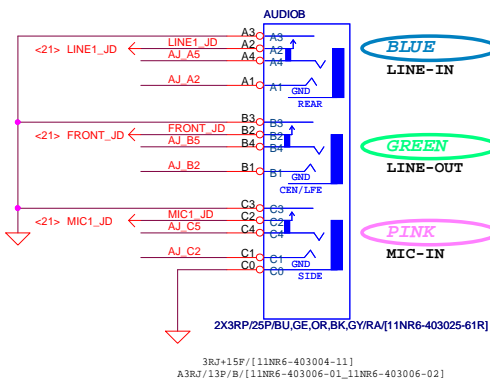
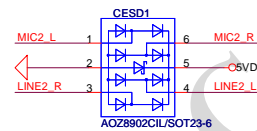


## SPDIF

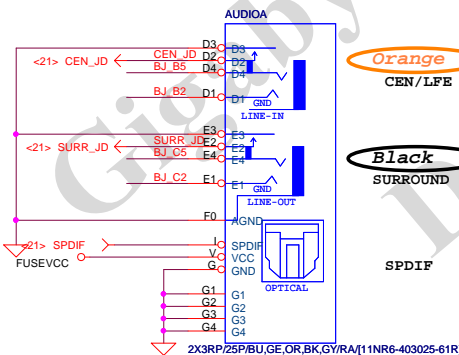


For Audio precision test

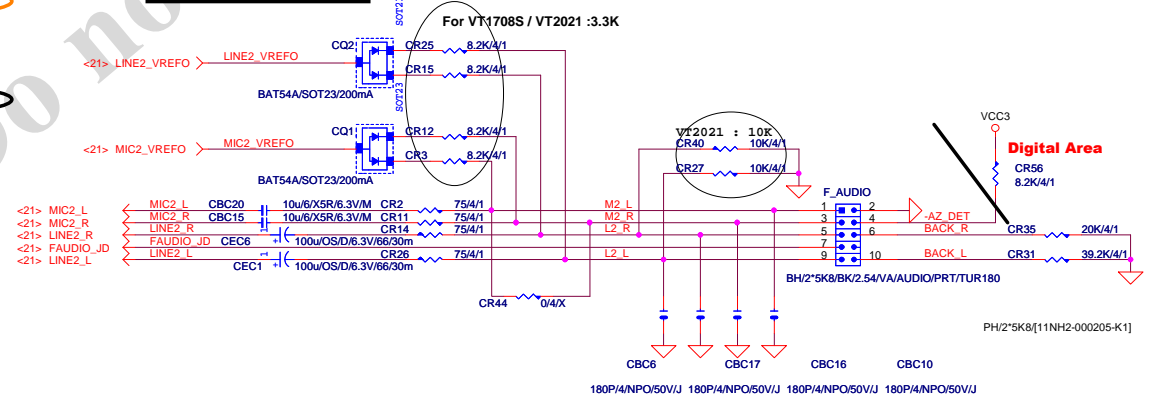
CBC38 0.01u/4/X7R/25V/K



3R3+15P/[11NR6-403004-11]  
A3R3/13P/B/[11NR6-403006-01\_11NR6-403006-02]



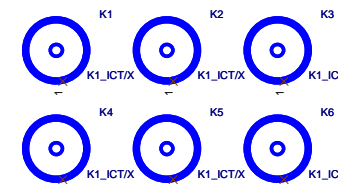
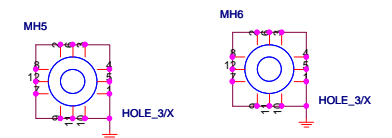
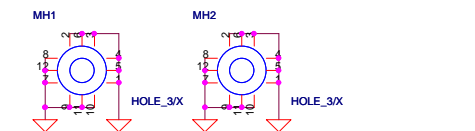
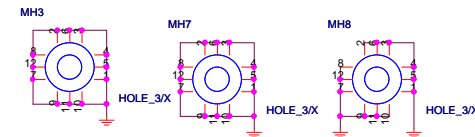
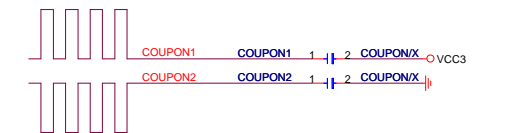
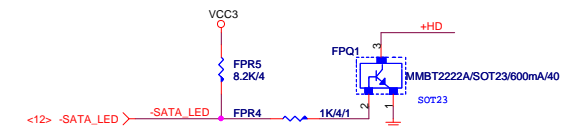
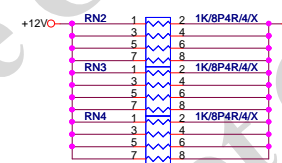
INTEL FRONT AUDIO



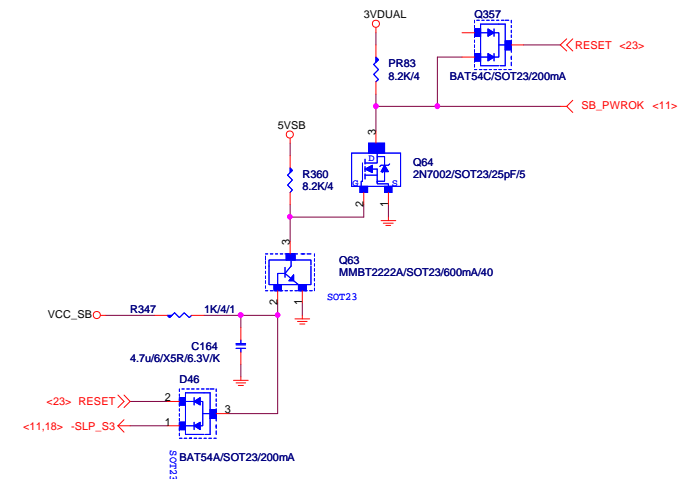
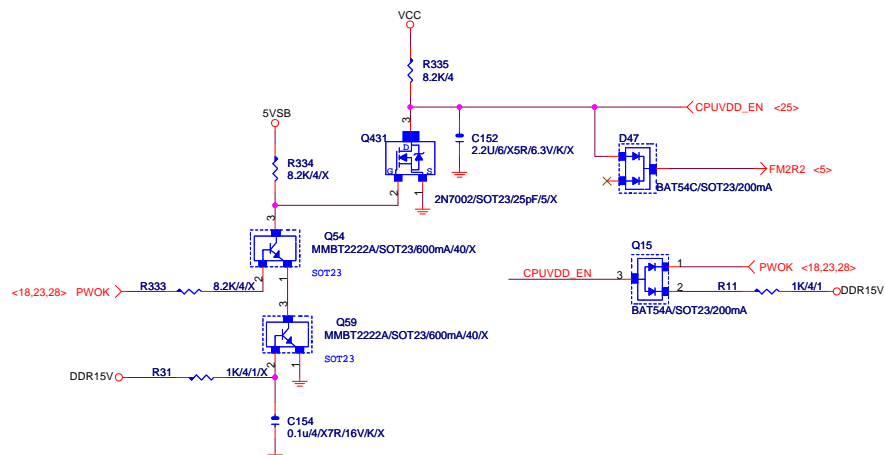
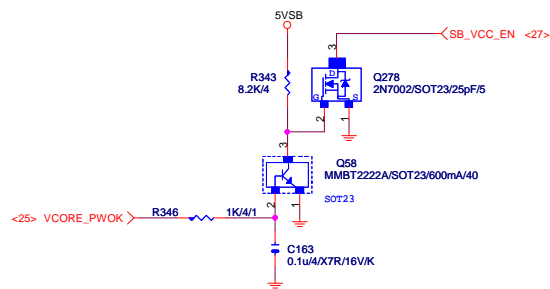
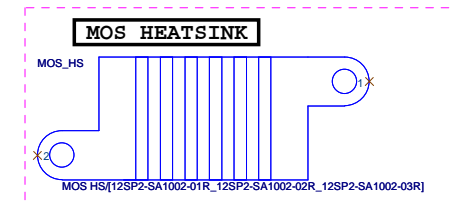
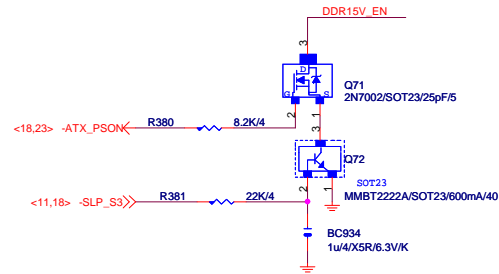
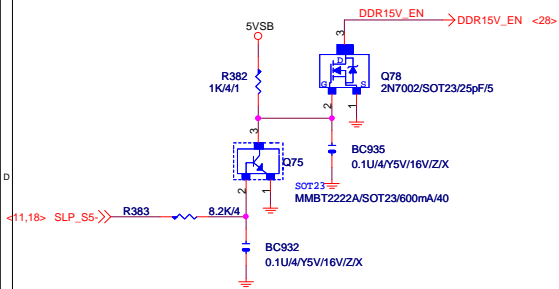
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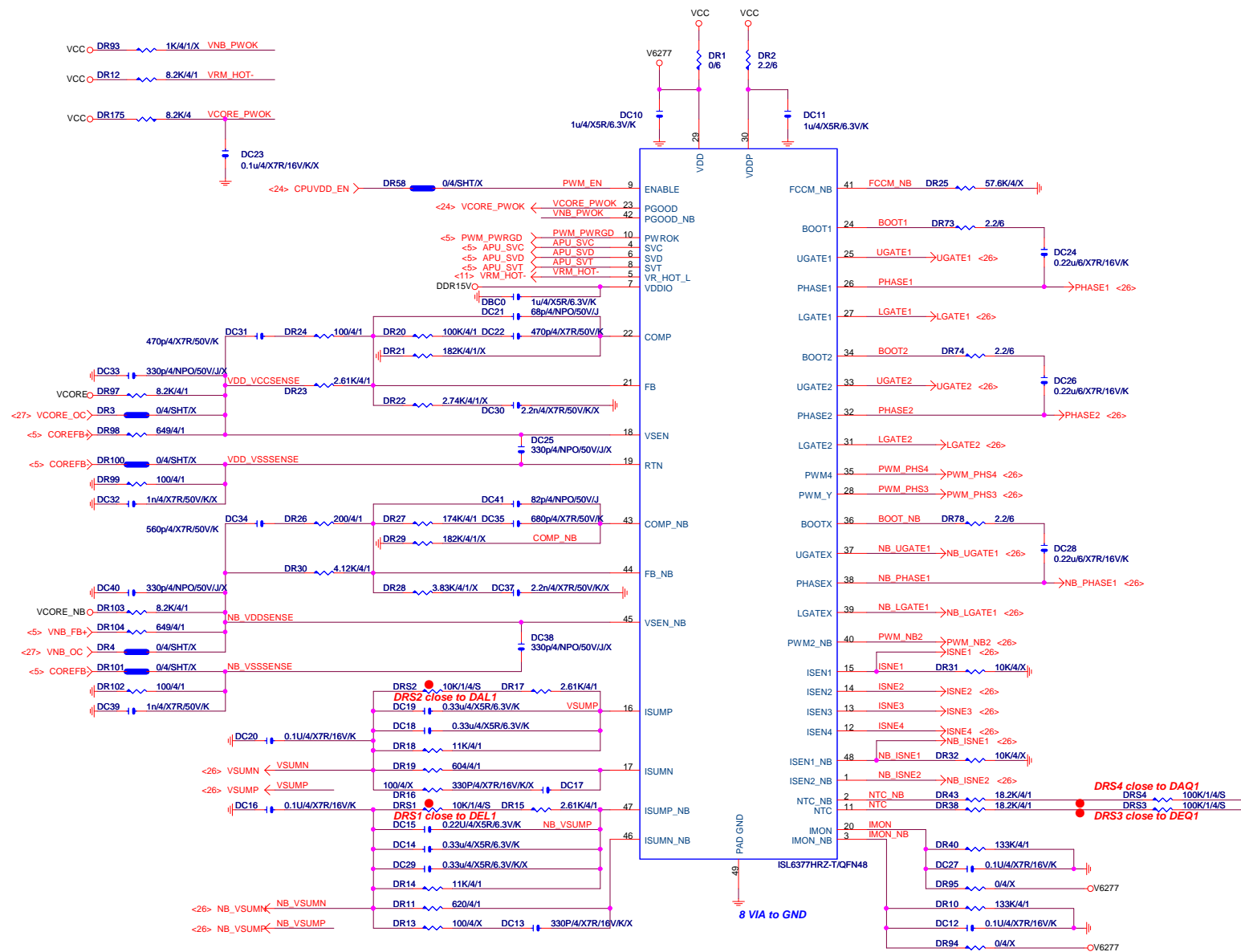


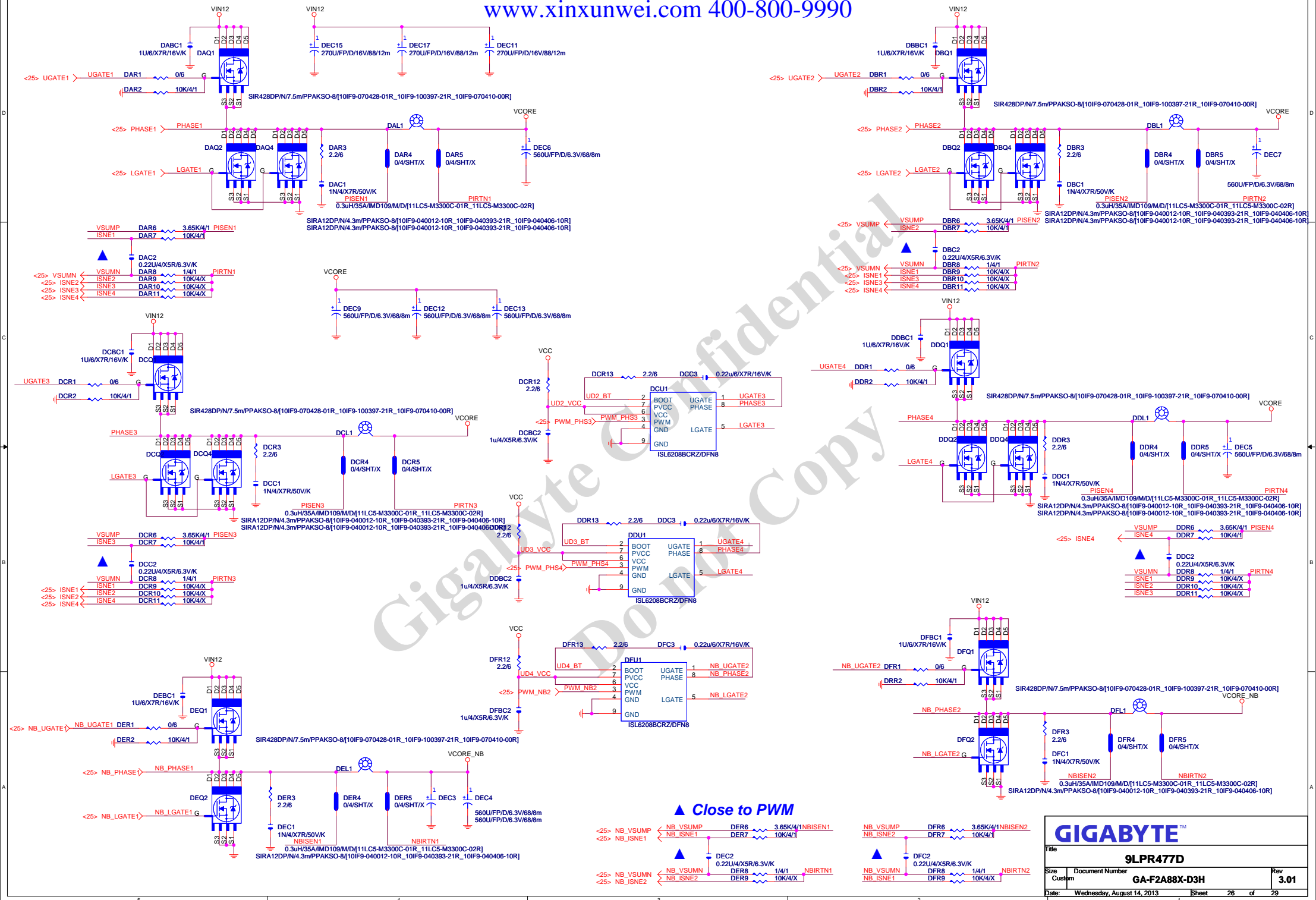


For Seasonic 900W  
Power supply  
cant Boot issue

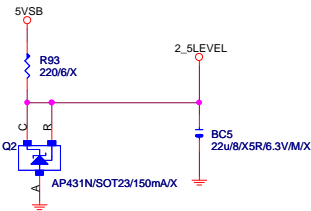
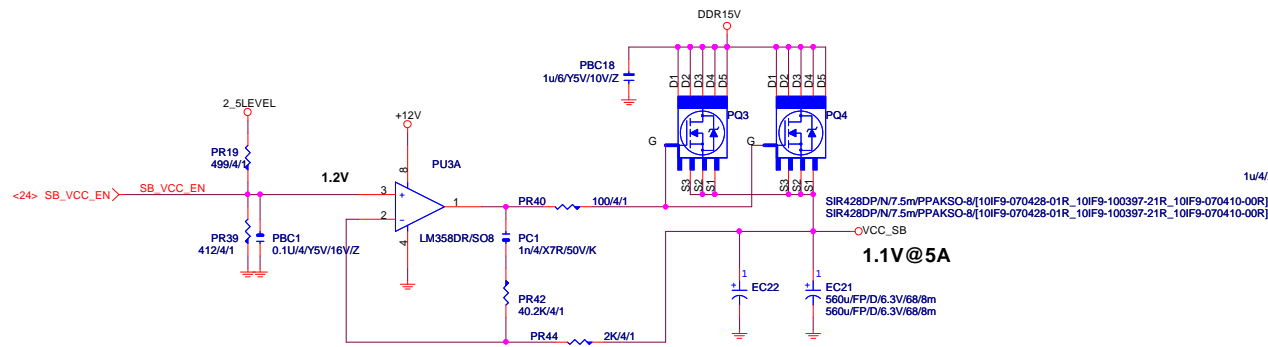


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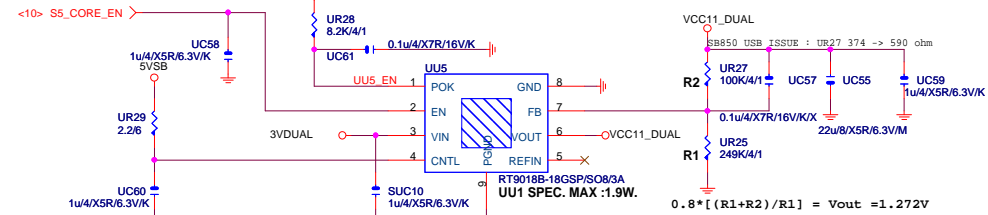
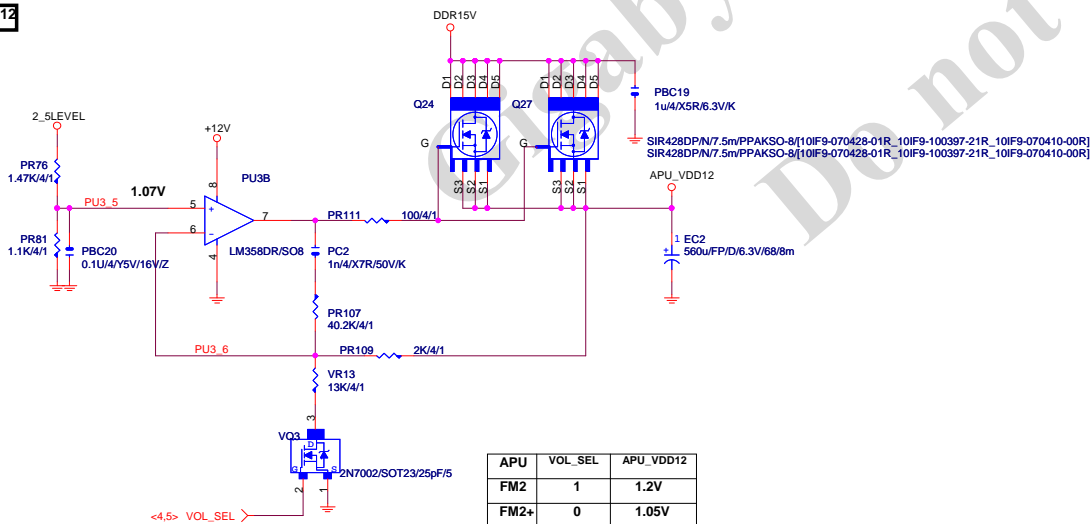




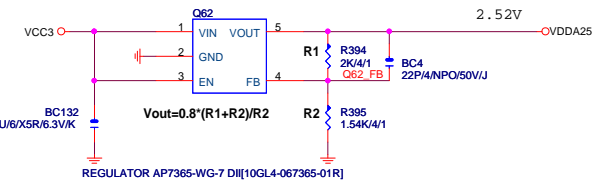
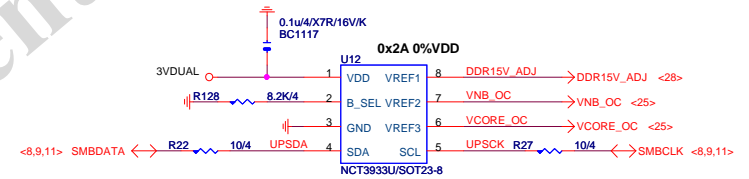
VCC\_SB



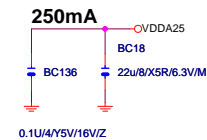
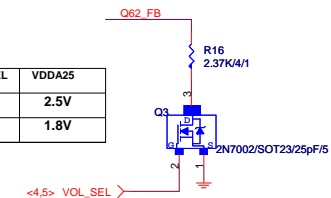
APU\_VDD12



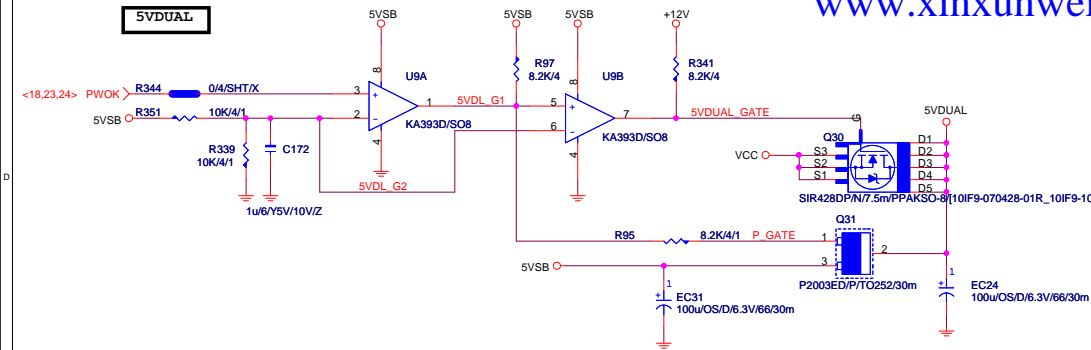
【技術通報R&D技術通報156】  
RT9018(RICHTER)與NCT3730(NUVOTON),  
EM5103GE(EMC)做共用,針對PIN7(FB)分壓阻值部份  
(R1/R2)須做修改為100K以上電阻值



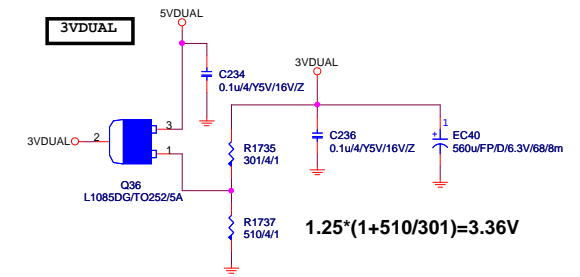
APU	VOL_SEL	VDDA25
FM2	1	2.5V
FM2+	0	1.8V



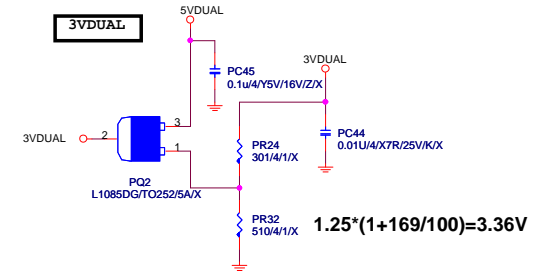
### 5VDUAL



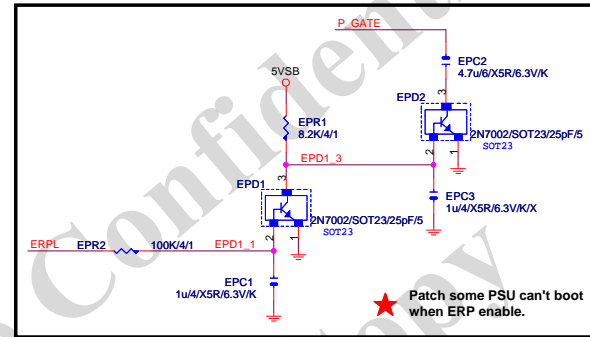
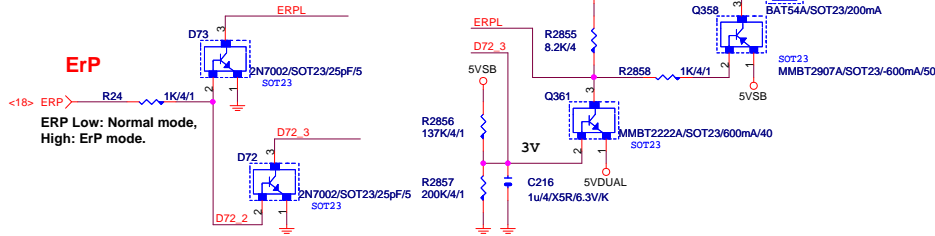
### 3VDUAL



### 3VDUAL

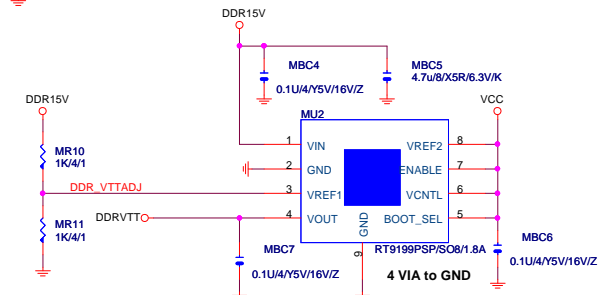
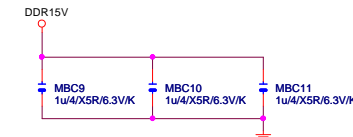
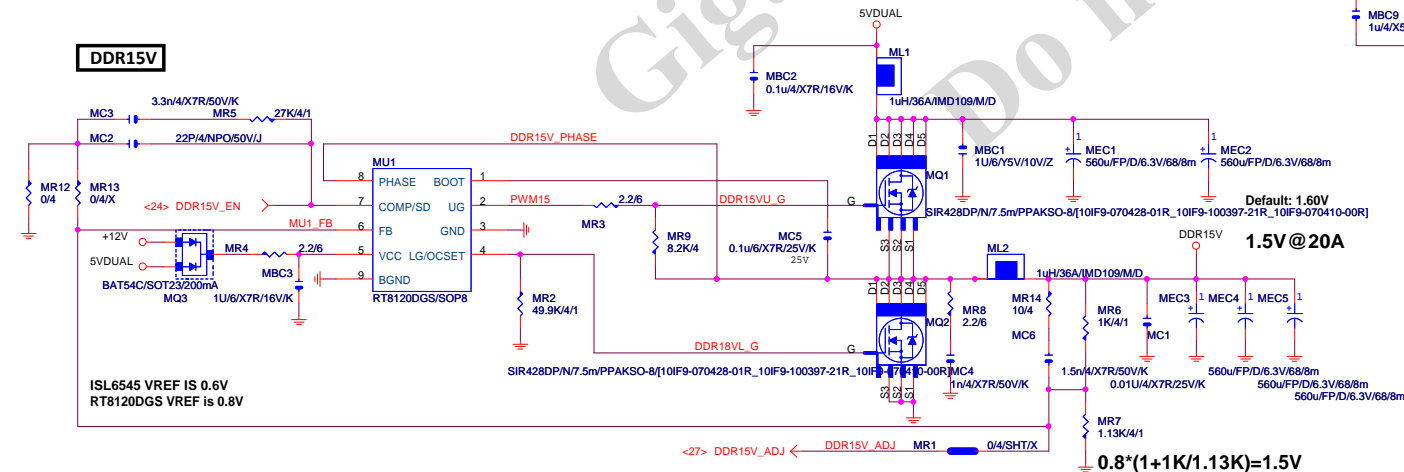


### ErP



★ Patch some PSU can't boot when ERP enable.

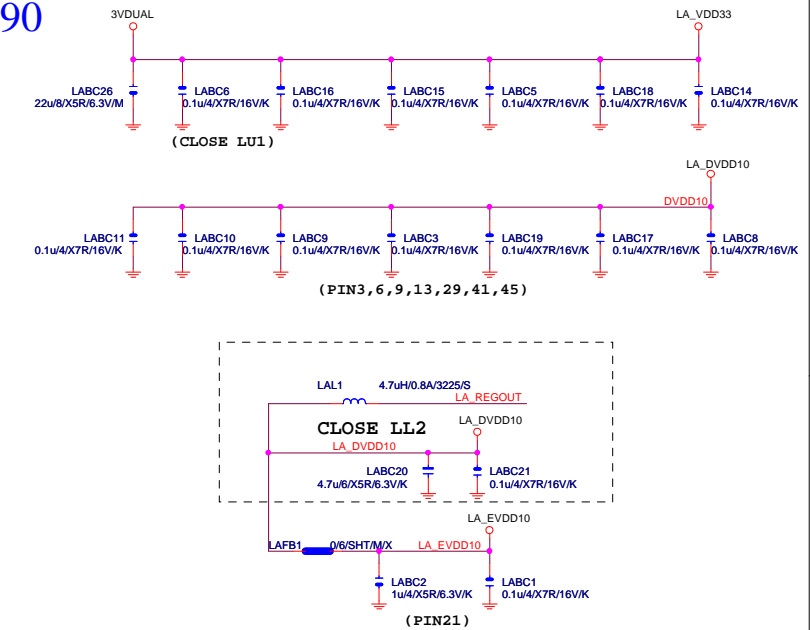
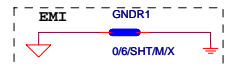
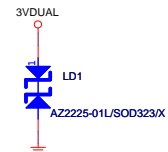
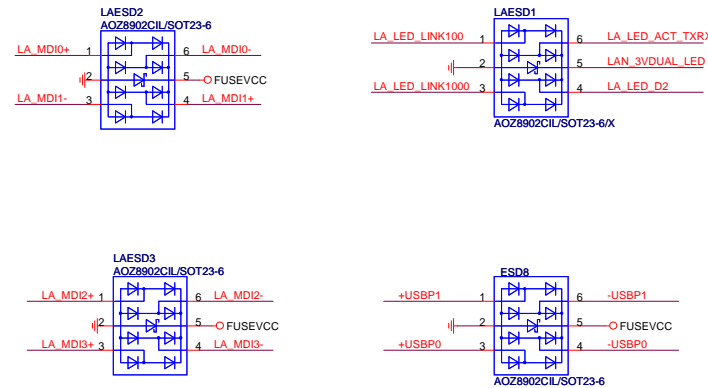
### DDR15V



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DDR PWR, 5VDUAL, ERP		
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	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

[illegible]**GIGABYTE™**

Title			
RTL8111E-VL			
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